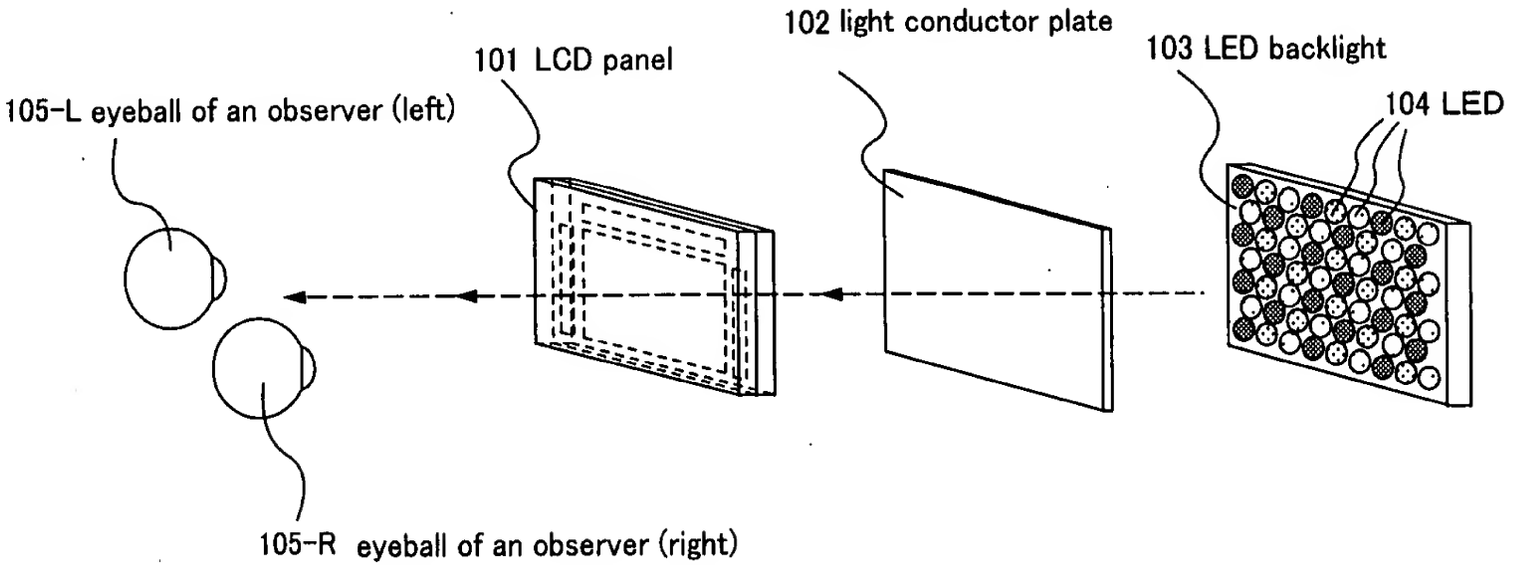

102 light conductor plate
101 LCD panel
103 LED backlight
104 LED
105-L eyeball of an observer (left)
105-R eyeball of an observer (right)

Fig. 1

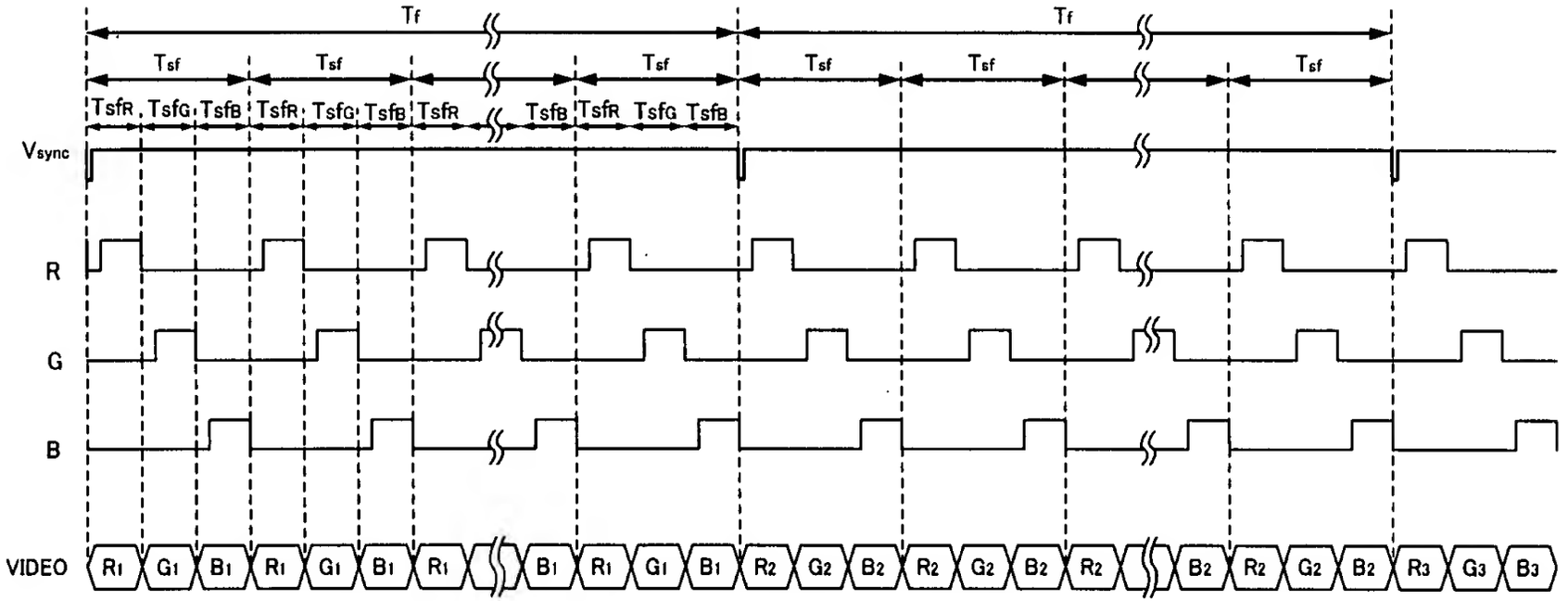

Tf
Tf
Tsf
Tsf
Tsf
Tsf
Tsf
Tsf
TsfR TsfG TsfB TsfR TsfG TsfB TsfR TsfB TsfR TsfG TsfB
Vsync
R
G
B
VIDEO
R1 G1 B1 R1 G1 B1 R1 B1 R1 G1 B1 R2 G2 B2 R2 G2 B2 R2 B2 R2 G2 B2 R3 G3 B3

Fig. 2

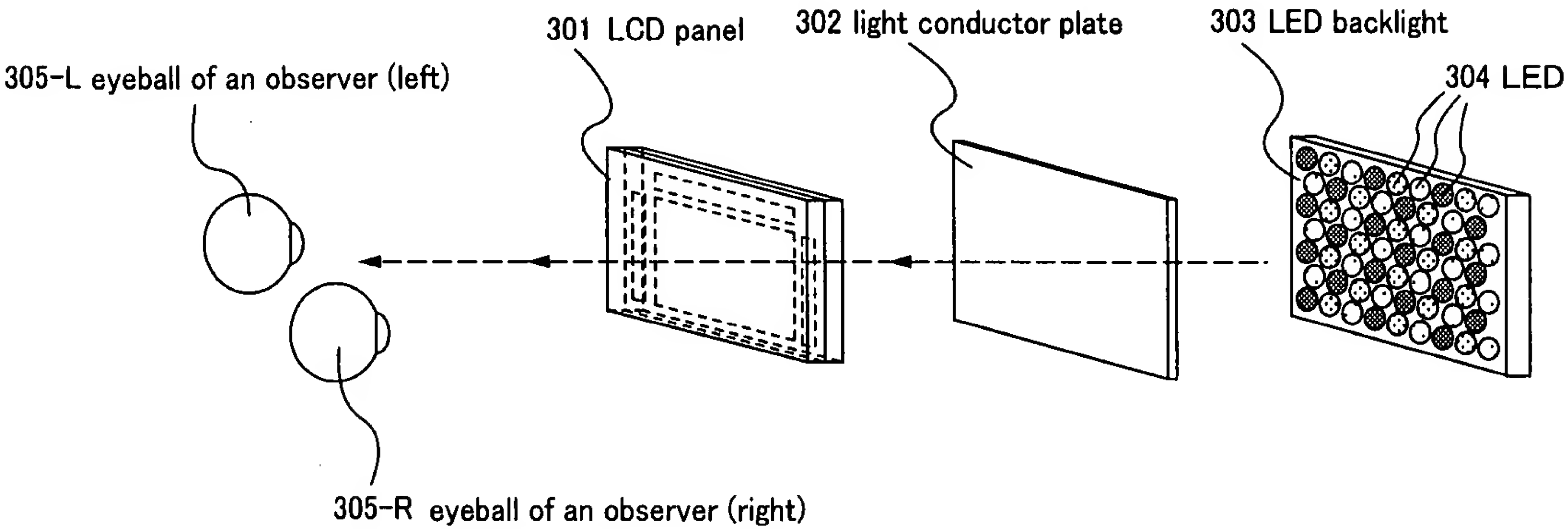

301 LCD panel
302 light conductor plate
303 LED backlight
304 LED
305-L eyeball of an observer (left)
305-R eyeball of an observer (right)

Fig. 3

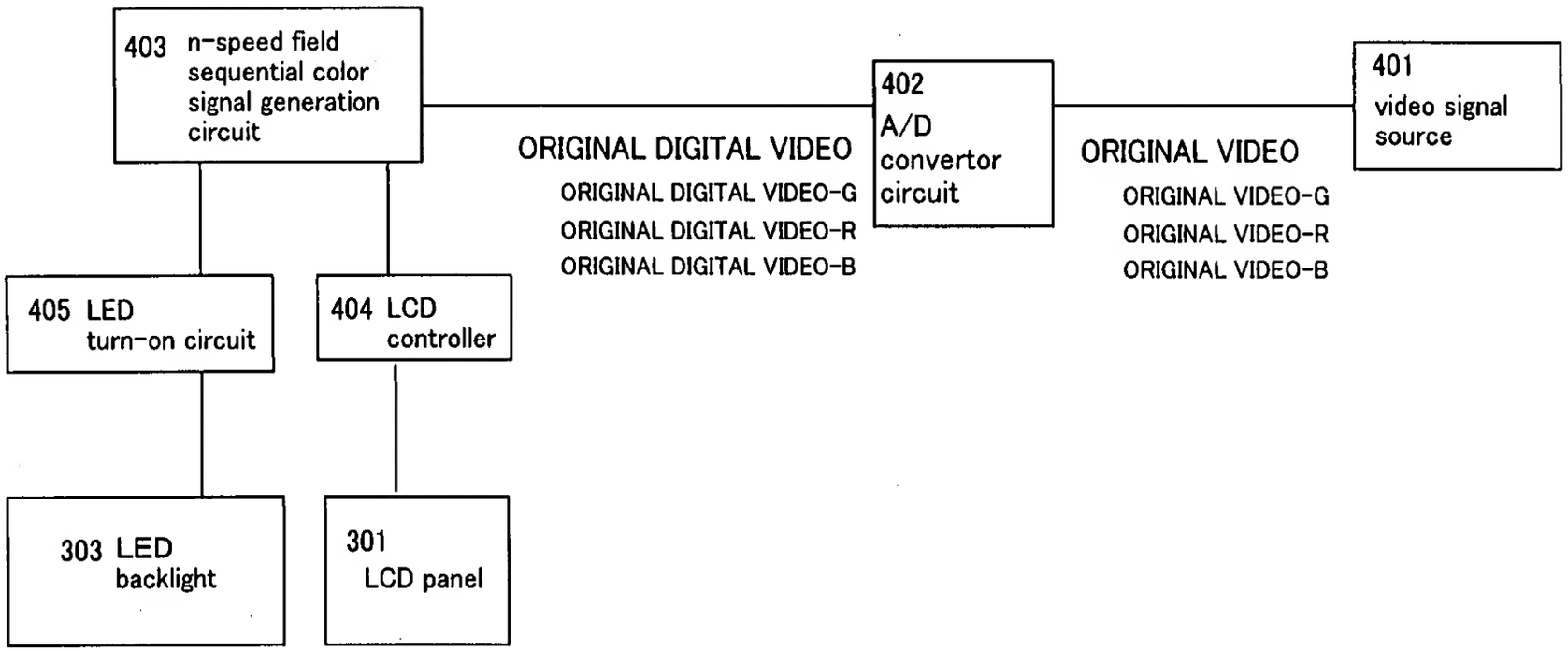

403 n-speed field sequential color signal generation circuit
ORIGINAL DIGITAL VIDEO
ORIGINAL DIGITAL VIDEO-G
ORIGINAL DIGITAL VIDEO-R
ORIGINAL DIGITAL VIDEO-B
402 A/D convertor circuit
ORIGINAL VIDEO
ORIGINAL VIDEO-G
ORIGINAL VIDEO-R
ORIGINAL VIDEO-B
401 video signal source
405 LED turn-on circuit
404 LCD controller
303 LED backlight
301 LCD panel

Fig. 4

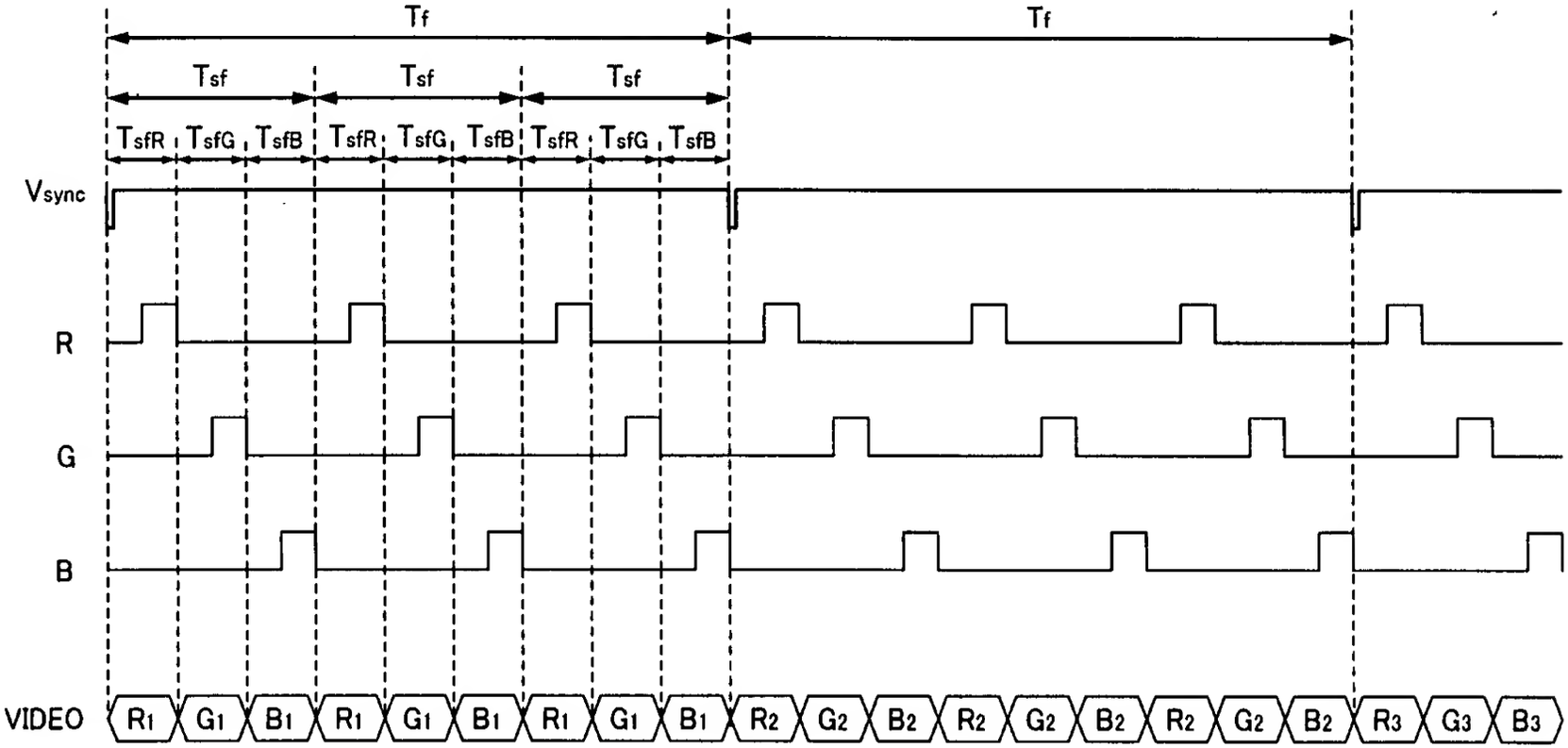

Tf
Tf
Tsf
Tsf
Tsf
TsfR
TsfG
TsfB
TsfR
TsfG
TsfB
TsfR
TsfG
TsfB
Vsync
R
G
B
VIDEO
R1
G1
B1
R1
G1
B1
R1
G1
B1
R2
G2
B2
R2
G2
B2
R2
G2
B2
R3
G3
B3

Fig. 5

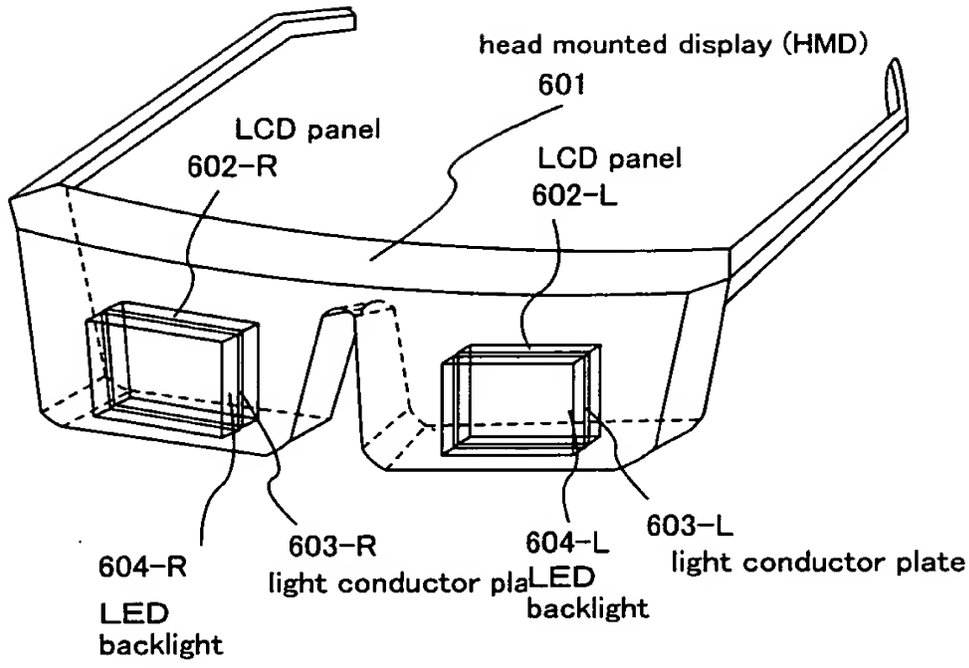

head mounted display (HMD)
601
LCD panel
602-R
LCD panel
602-L
604-R
LED
backlight
603-R
light conductor pla
604-L
LED
backlight
603-L
light conductor plate

Fig. 6

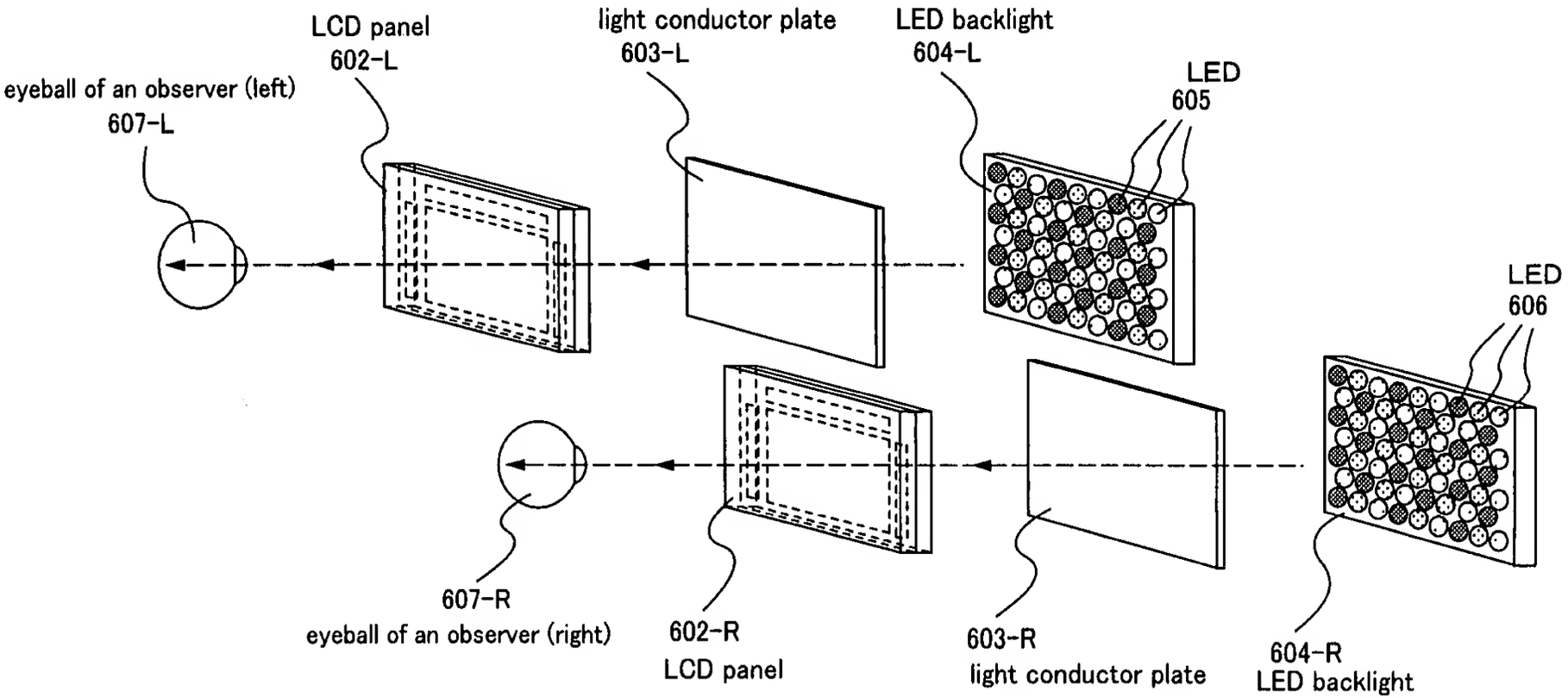

LCD panel
602-L
light conductor plate
603-L
LED backlight
604-L
LED
605
eyeball of an observer (left)
607-L
LED
606
607-R
eyeball of an observer (right)
602-R
LCD panel
603-R
light conductor plate
604-R
LED backlight

Fig. 7

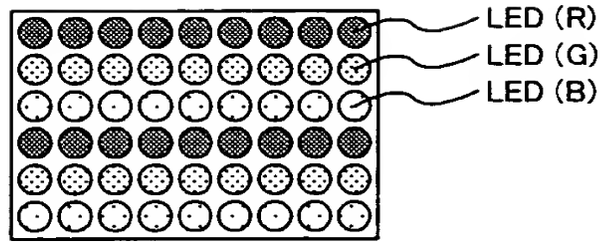


Fig. 8(A)

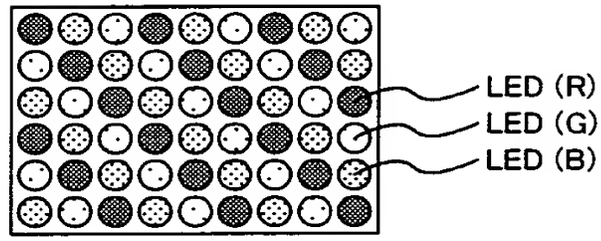


Fig. 8(B)

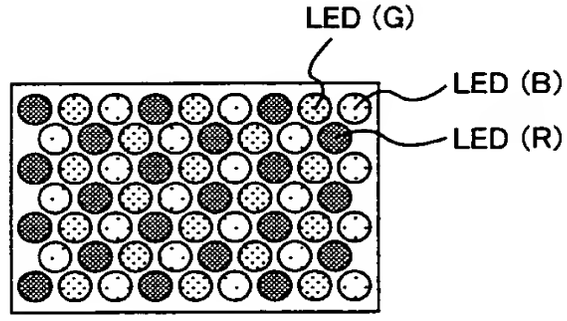


Fig. 8(C)

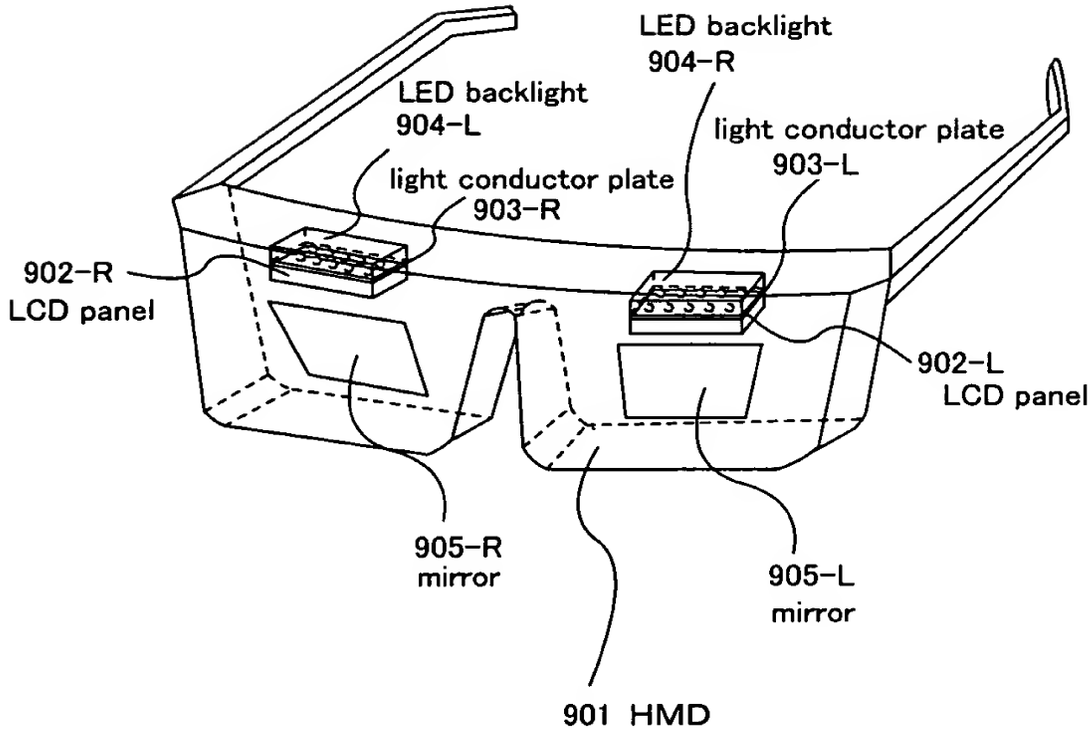

LED backlight
904-R
LED backlight
904-L
light conductor plate
903-L
light conductor plate
903-R
902-R
LCD panel
902-L
LCD panel
905-R
mirror
905-L
mirror
901 HMD

Fig. 9

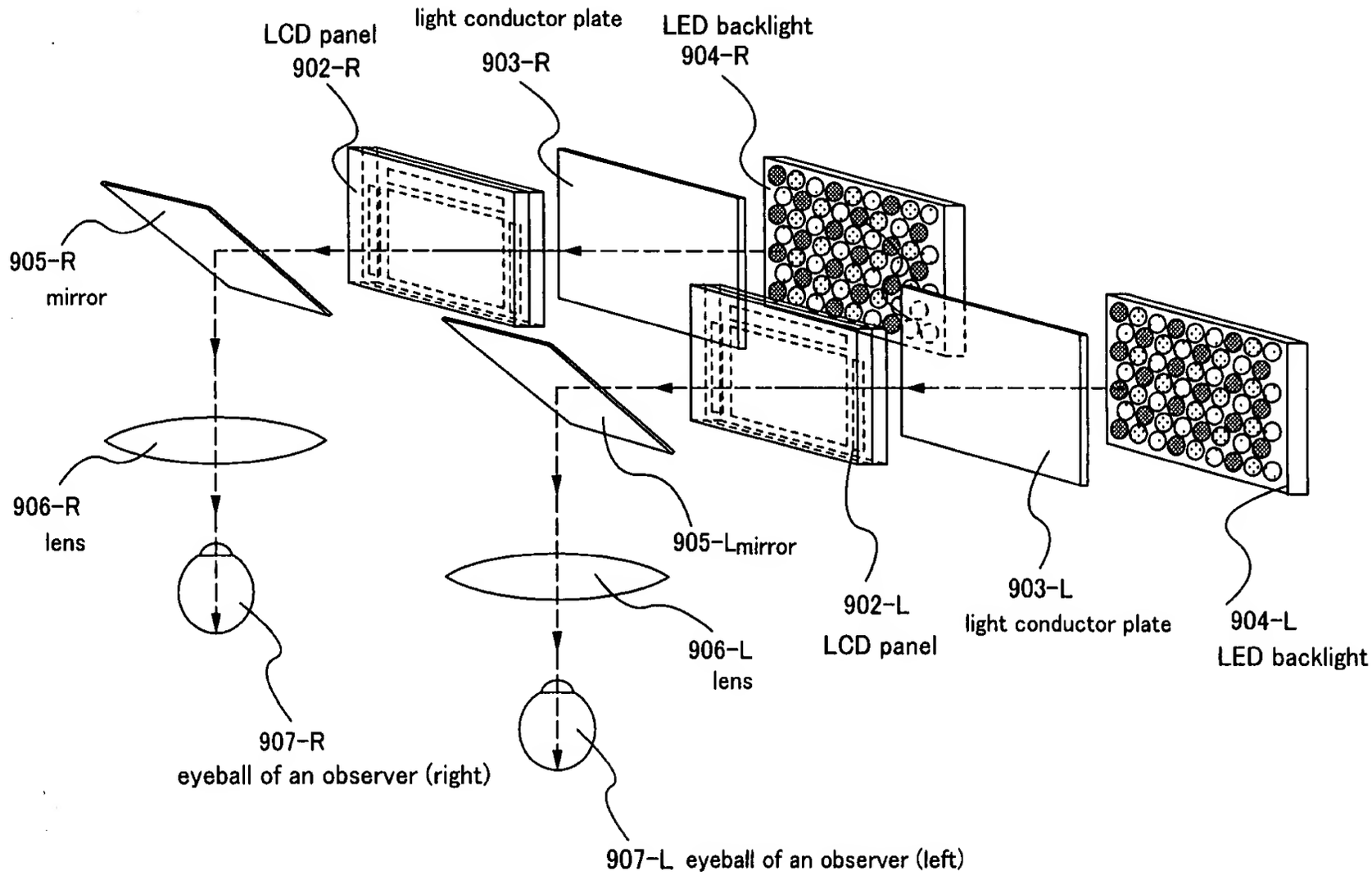

LCD panel
902-R
light conductor plate
903-R
LED backlight
904-R
905-R
mirror
906-R
lens
907-R
eyeball of an observer (right)
905-Lmirror
906-L
lens
907-L eyeball of an observer (left)
902-L
LCD panel
903-L
light conductor plate
904-L
LED backlight

Fig. 10

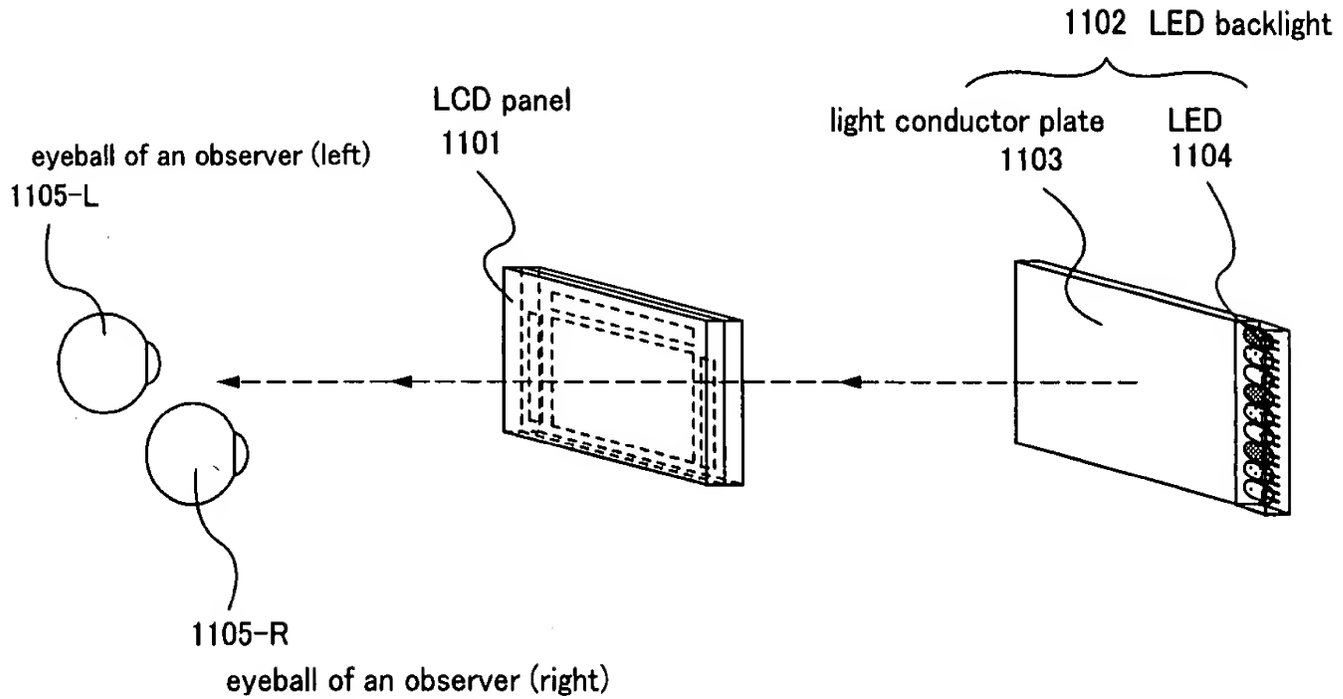

1102 LED backlight
LCD panel
1101
light conductor plate
1103
LED
1104
eyeball of an observer (left)
1105-L
1105-R
eyeball of an observer (right)

Fig. 11

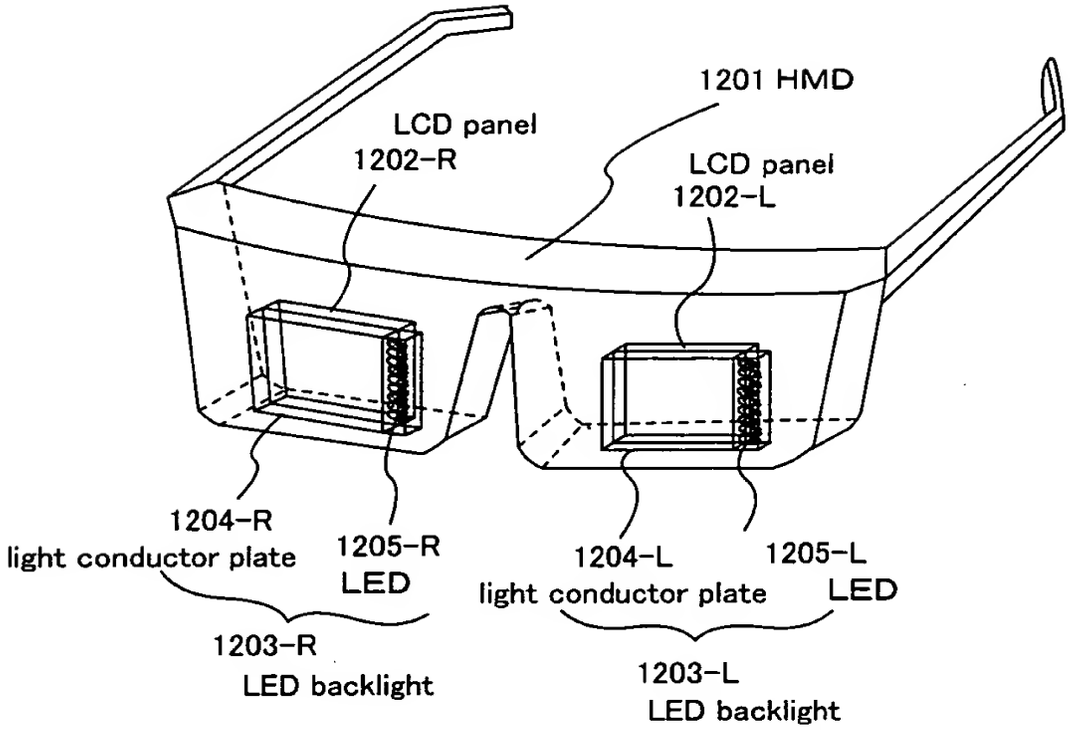

1201 HMD
LCD panel
1202-R
LCD panel
1202-L
1204-R
light conductor plate
1205-R
LED
1204-L
light conductor plate
1205-L
LED
1203-R
LED backlight
1203-L
LED backlight

Fig. 12

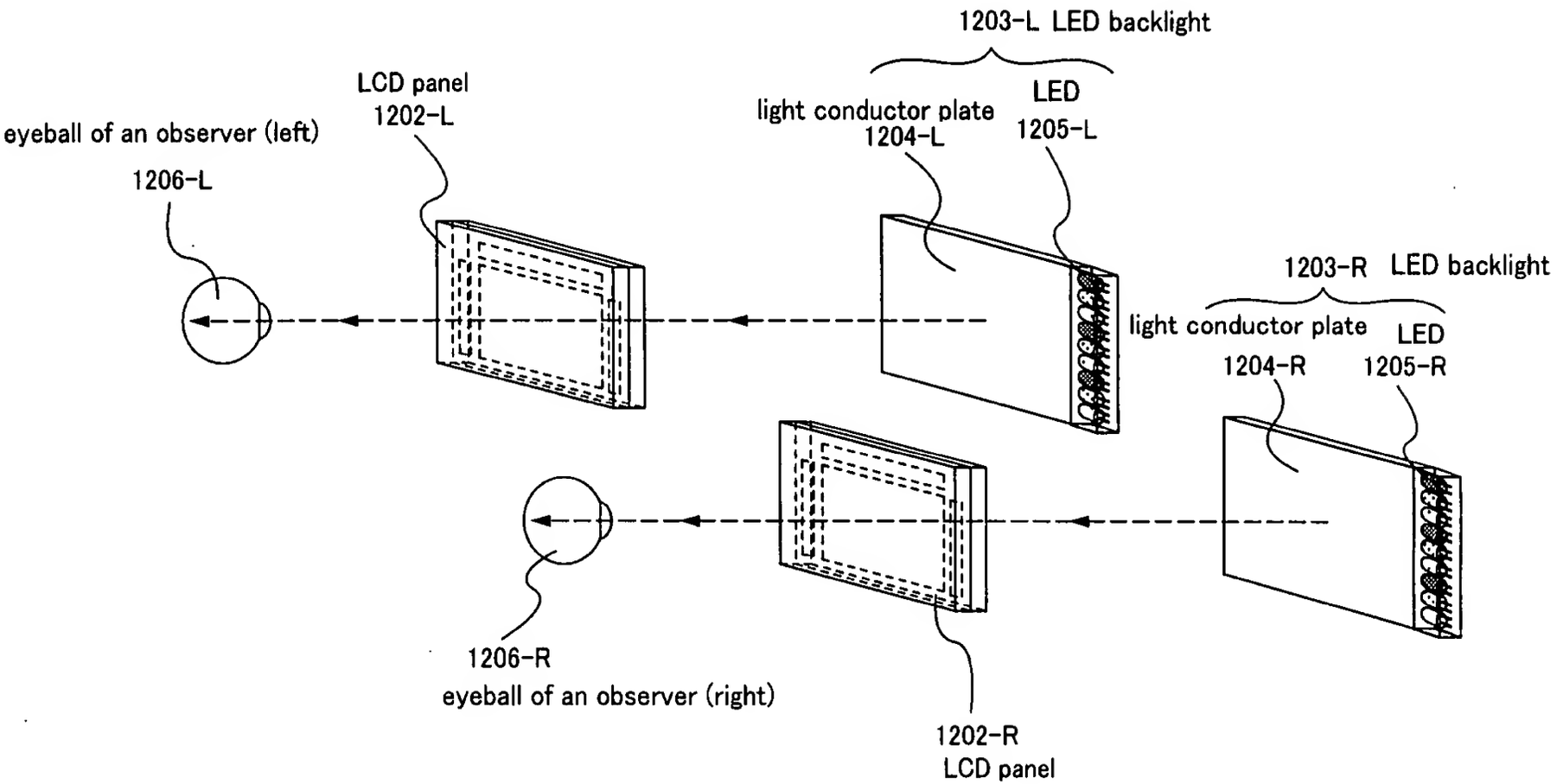

1203-L LED backlight
LCD panel
1202-L
eyeball of an observer (left)
1206-L
light conductor plate
1204-L
LED
1205-L
1203-R LED backlight
light conductor plate
LED
1204-R
1205-R
1206-R
eyeball of an observer (right)
1202-R
LCD panel

Fig. 13

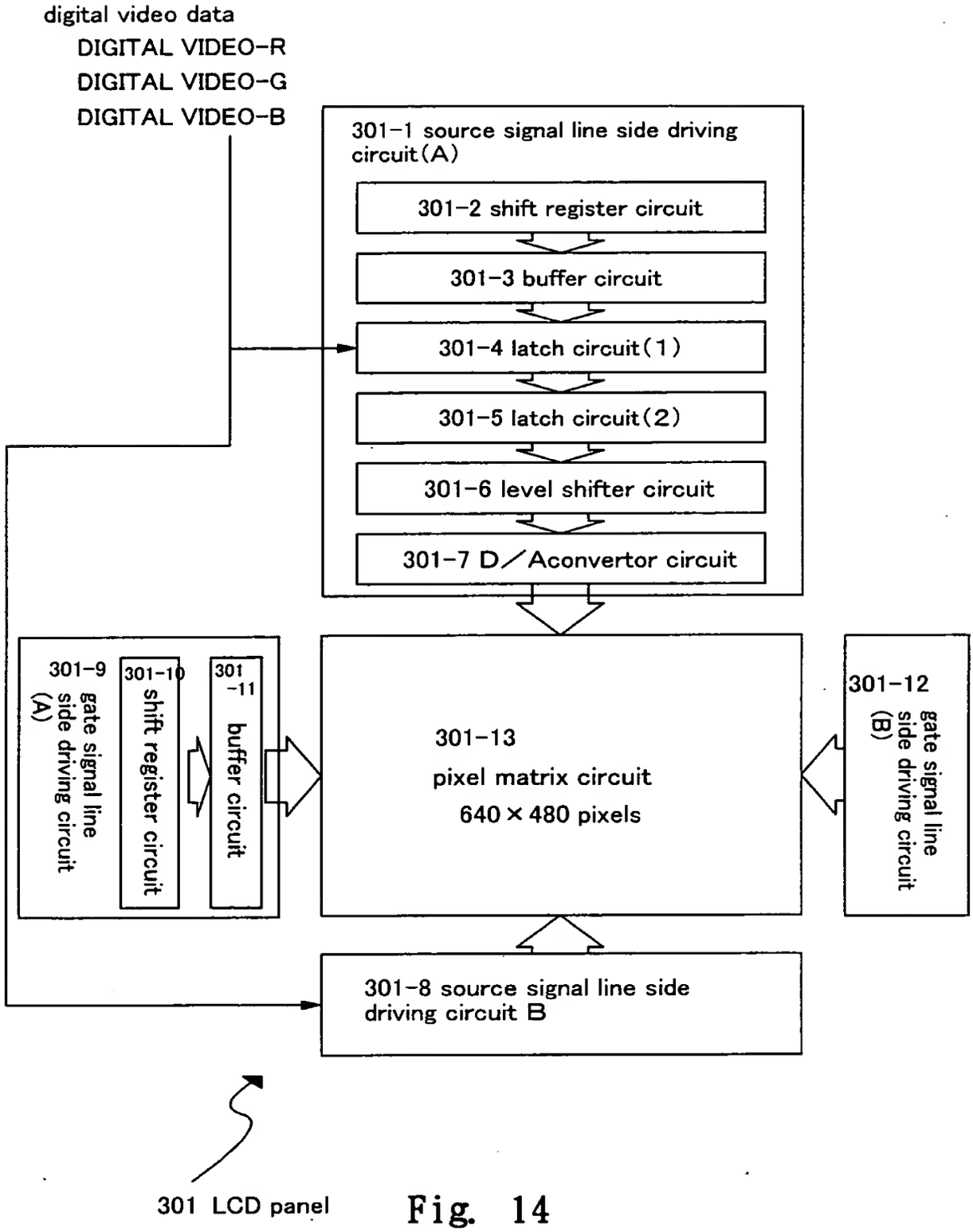

digital video data
DIGITAL VIDEO-R
DIGITAL VIDEO-G
DIGITAL VIDEO-B
301-1 source signal line side driving circuit(A)
301-2 shift register circuit
301-3 buffer circuit
301-4 latch circuit(1)
301-5 latch circuit(2)
301-6 level shifter circuit
301-7 D/Aconvertor circuit
301-9 gate signal line side driving circuit (A)
301-10 shift register circuit
301-11 buffer circuit
301-13
pixel matrix circuit
640 × 480 pixels
301-12 gate signal line side driving circuit (B)
301-8 source signal line side driving circuit B
301 LCD panel

Fig. 14

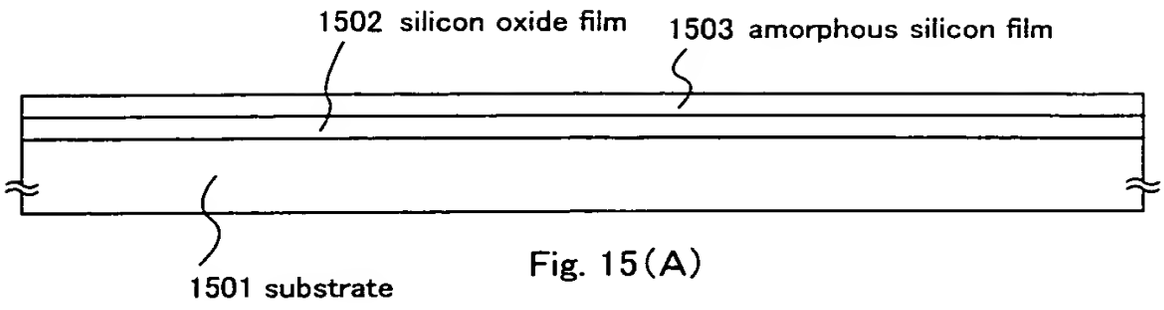


Fig. 15(A)

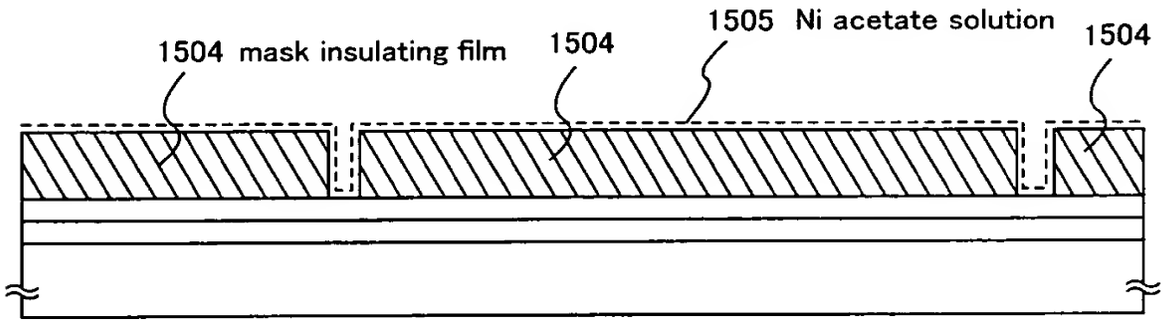


Fig. 15(B)

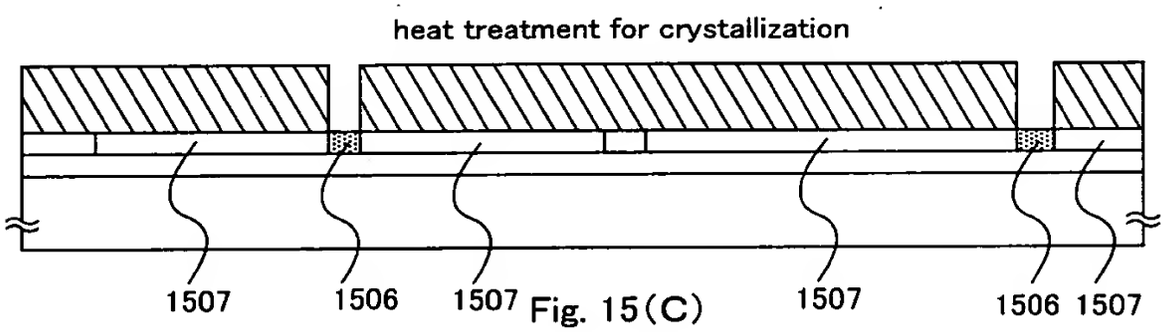


Fig. 15(C)

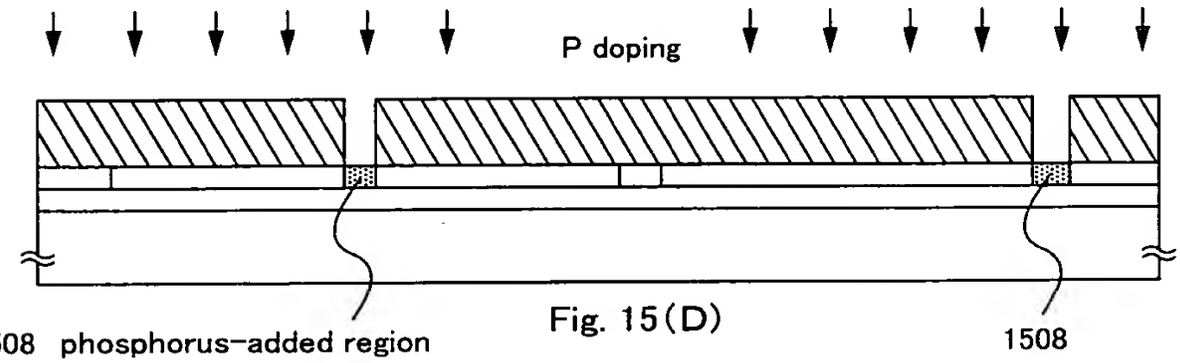


Fig. 15(D)

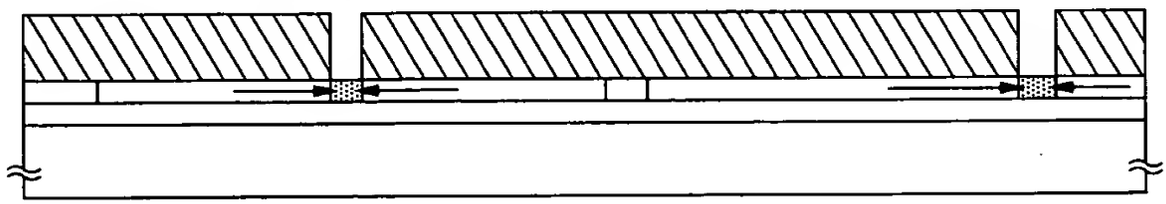


Fig.15(E)

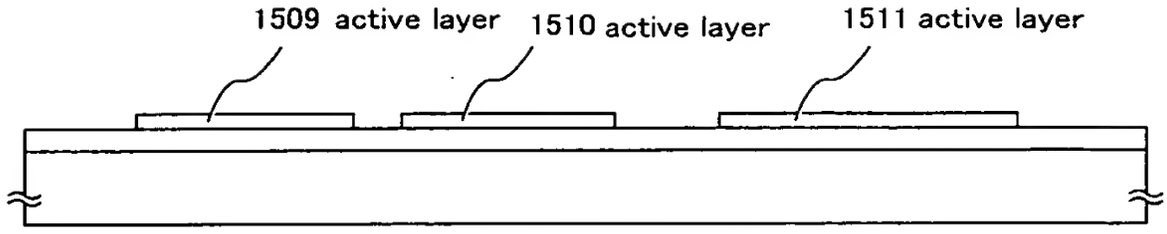


Fig. 16(A)

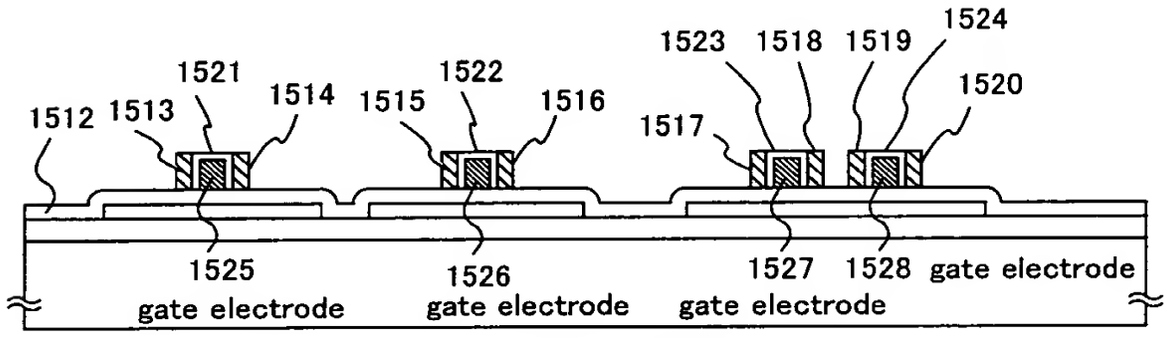


Fig. 16(B)

1513～1520 : porous anodic oxide films　　1521～1524 : non-porous anodic oxide films

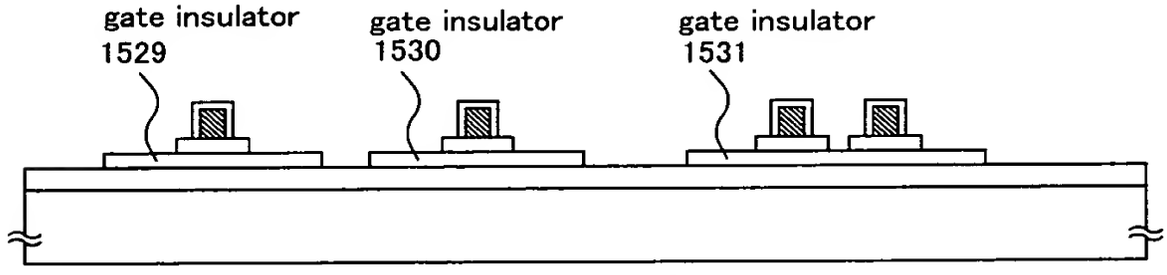


Fig. 16(C)

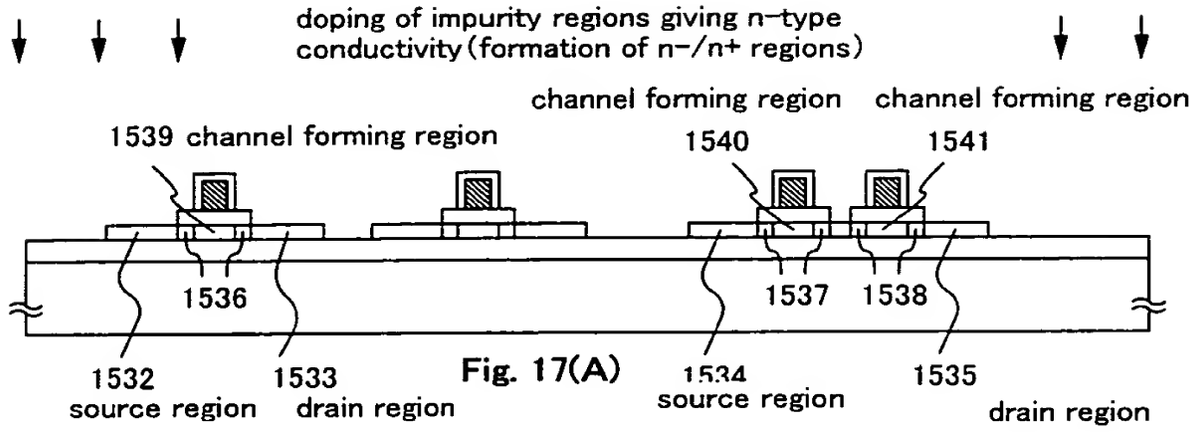


Fig. 17(A)

1536, 1537, 1538 : low concentration impurity region

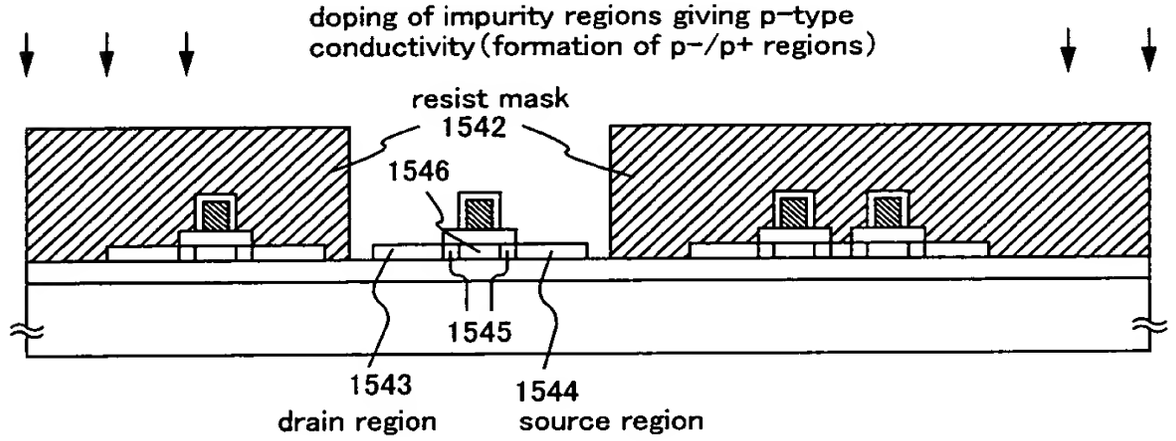


Fig. 17(B)

1545 : low concentration impurity region

1546 : channel forming region

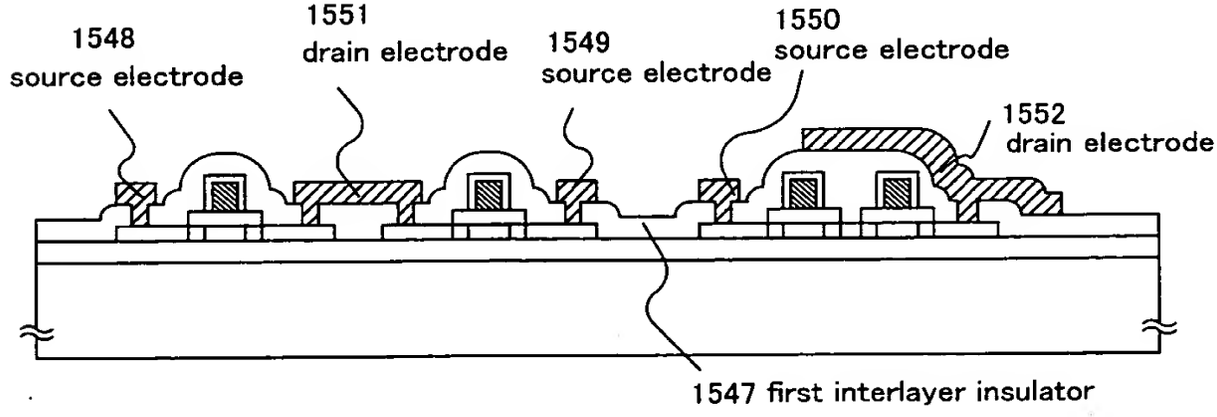


Fig. 17(C)

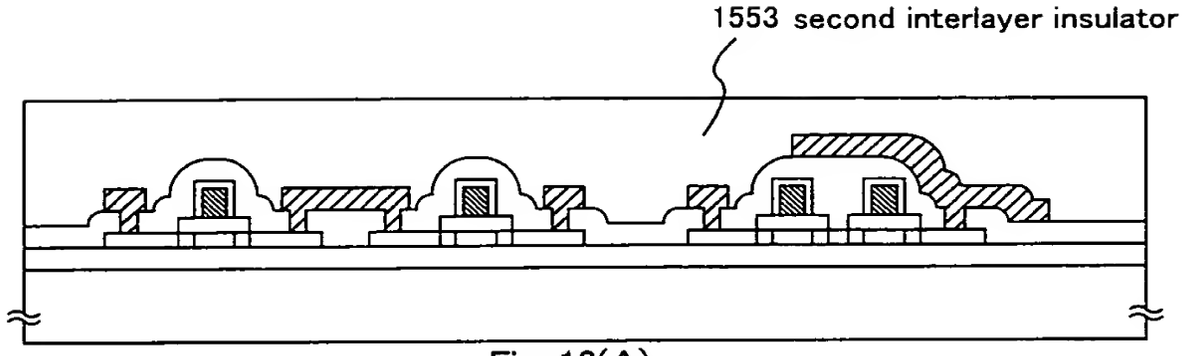


Fig. 18(A)

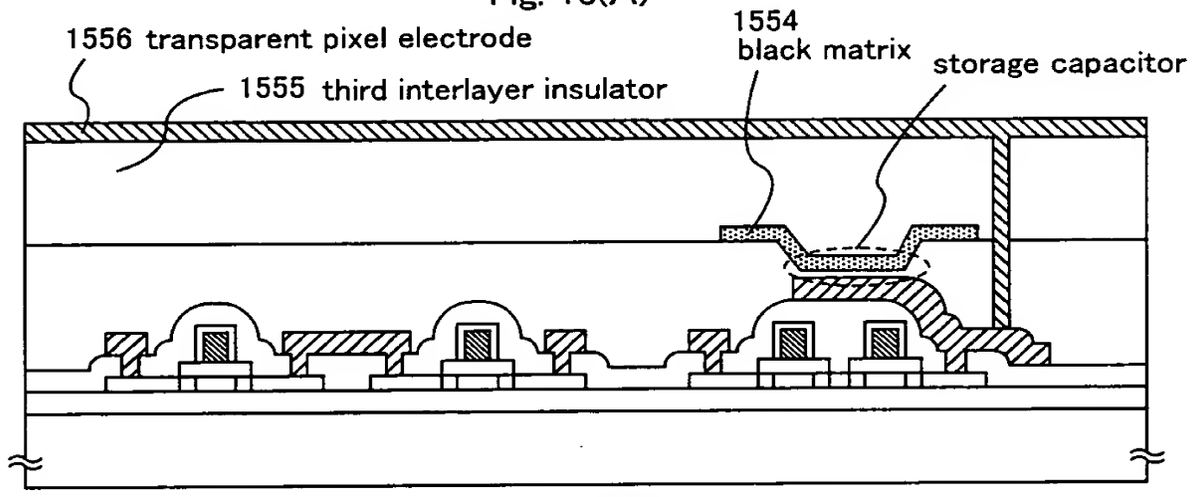


Fig. 18(B)

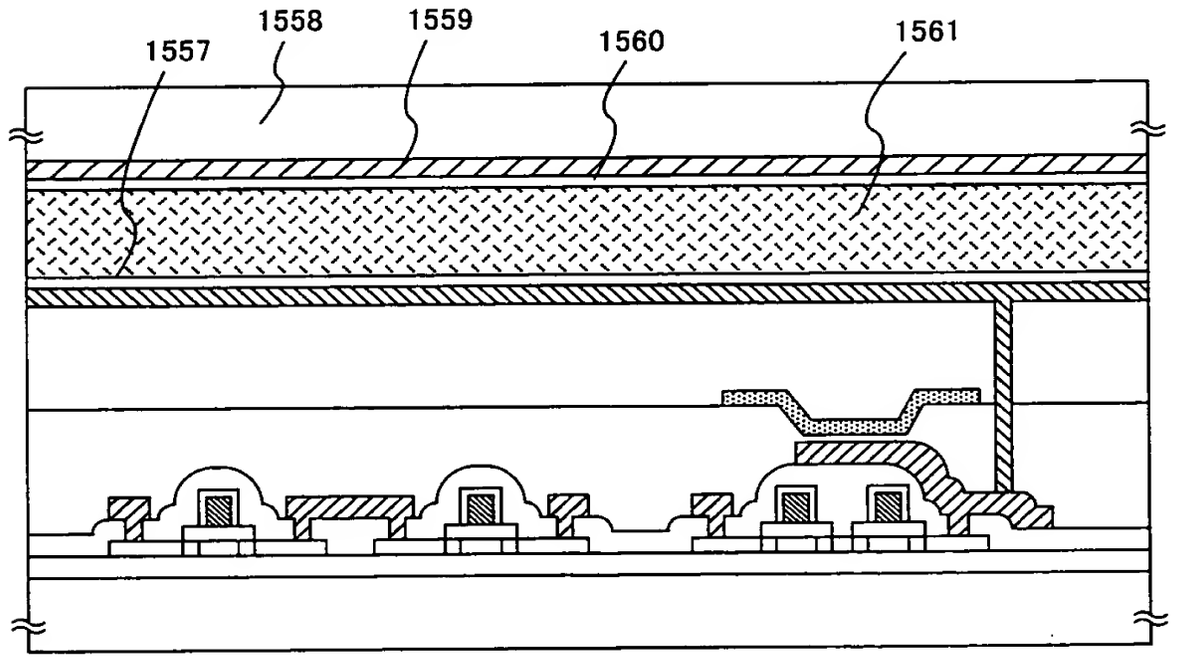
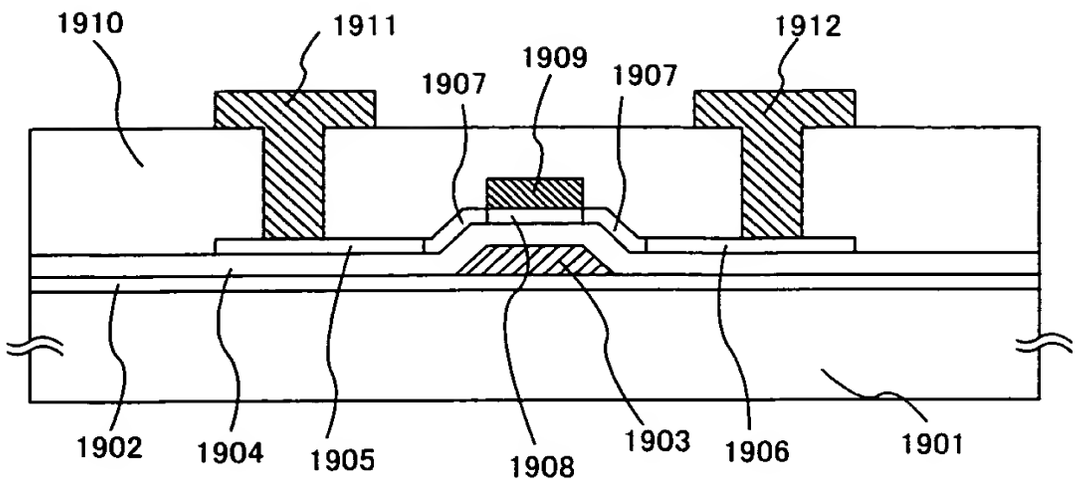
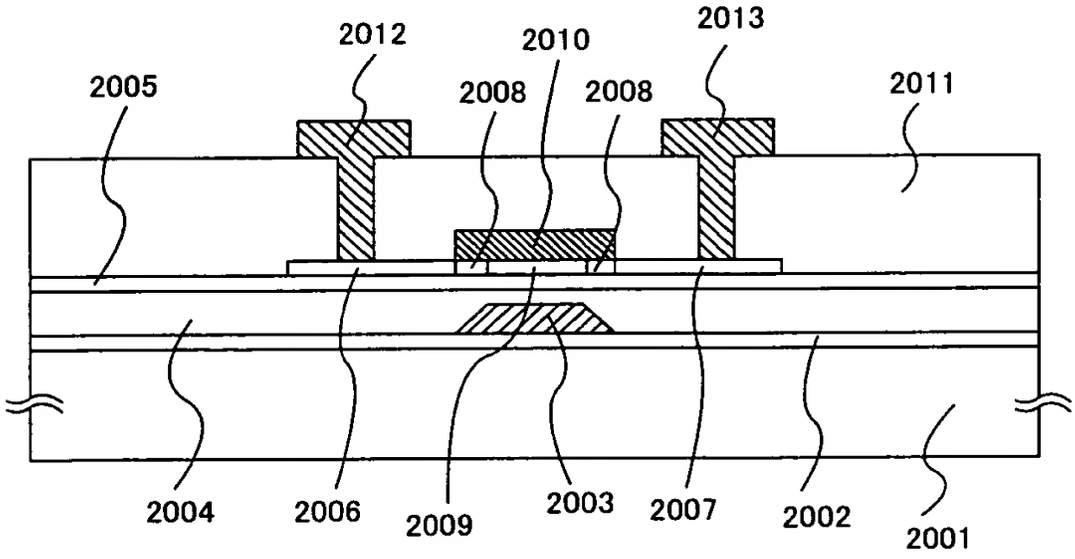


Fig. 18(C)



1901	substrate	1907	LDD
1902	silicon oxide film	1908	channel forming region
1903	gate electrode	1909	channel protecting film
1904	gate insulating film	1910	interlayer insulator
1905	source region	1911	source electrode
1906	drain region	1912	drain electrode

Fig. 19



2001	substrate	2008	(LDD)
2002	silicon oxide film	2009	channel forming region
2003	gate electrode	2010	channel protecting film
2004	benzocyclobutene(BCB)	2011	interlayer insulator
2005	silicon nitride	2012	source electrode
2006	source region	2013	drain electrode
2007	drain region		

Fig. 20

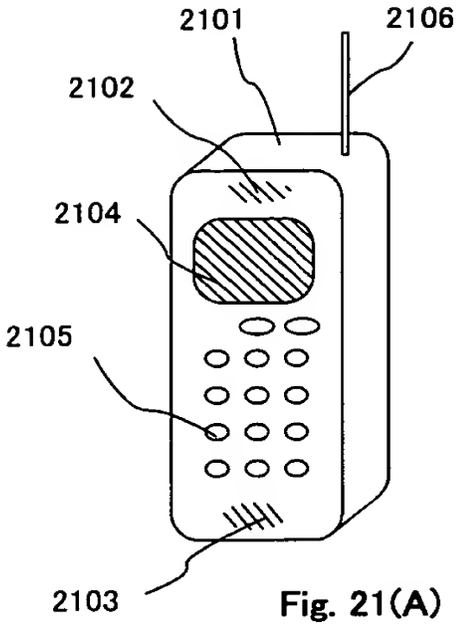


Fig. 21(A)

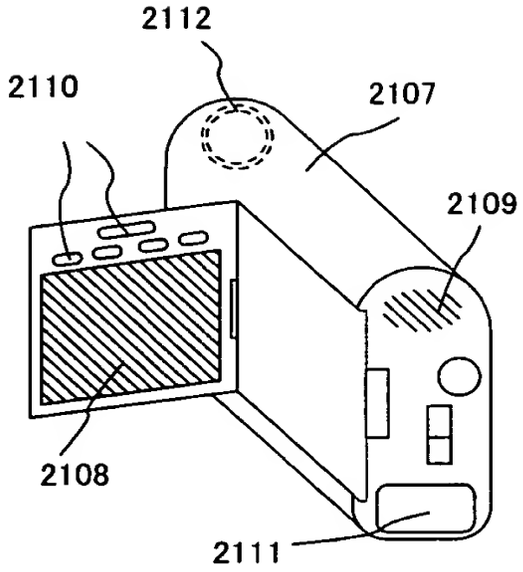


Fig. 21(B)

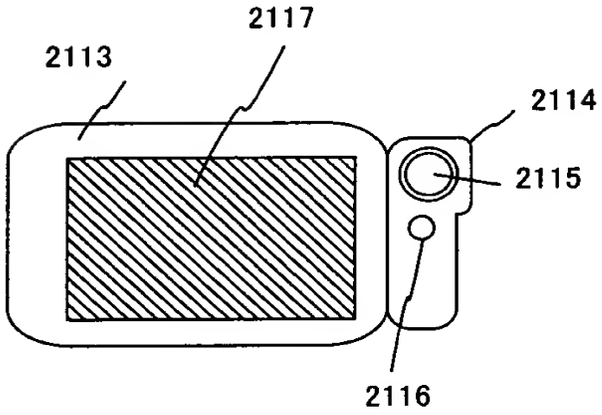


Fig. 21(C)

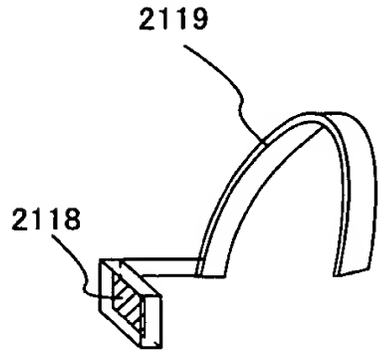


Fig. 21(D)

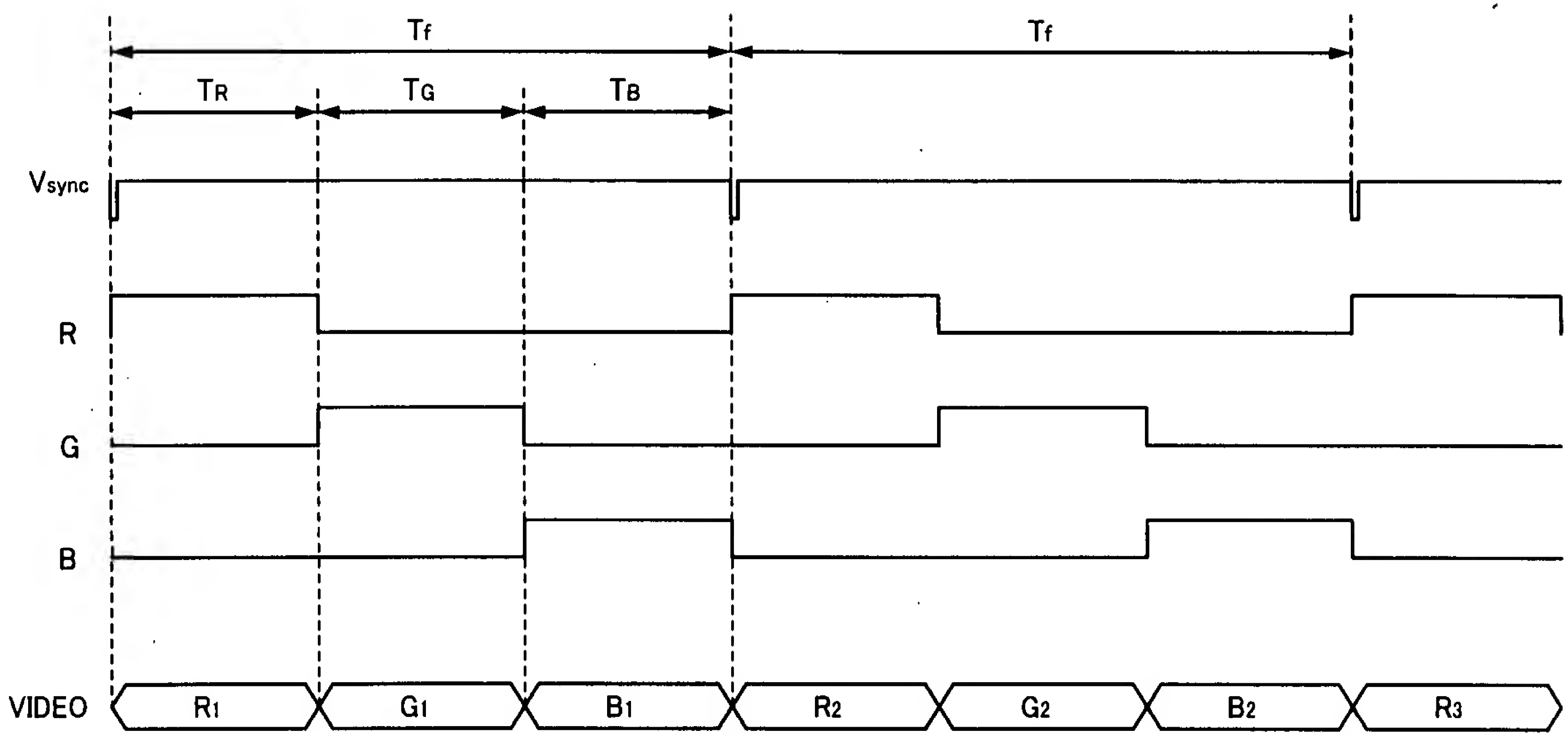


Fig. 22 Prior Art

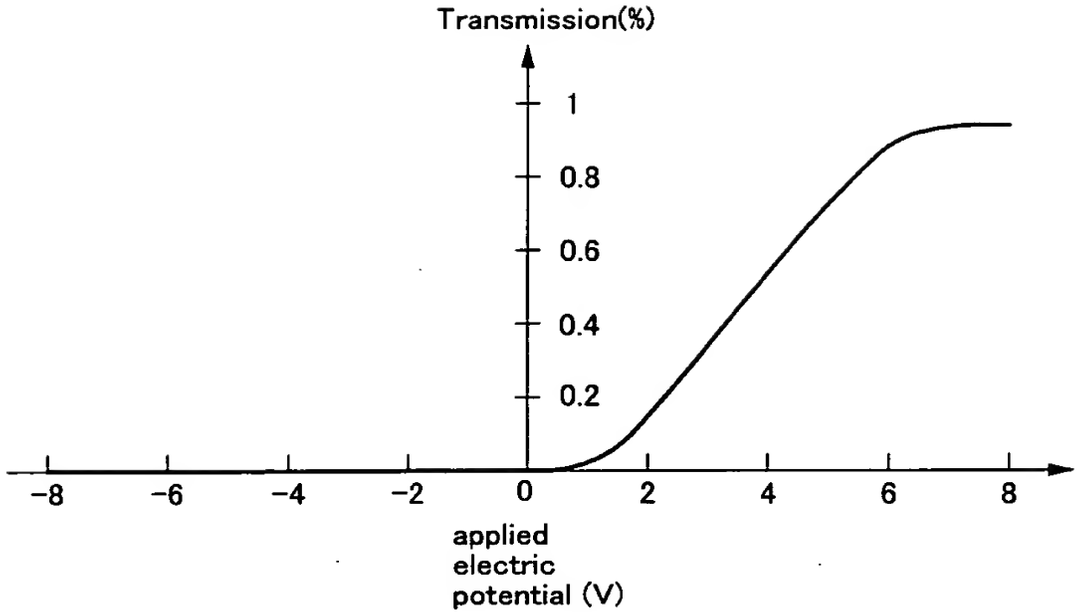

Transmission(%)
1
0.8
0.6
0.4
0.2
−8
−6
−4
−2
0
2
4
6
8
applied
electric
potential (V)

Fig. 23(A)

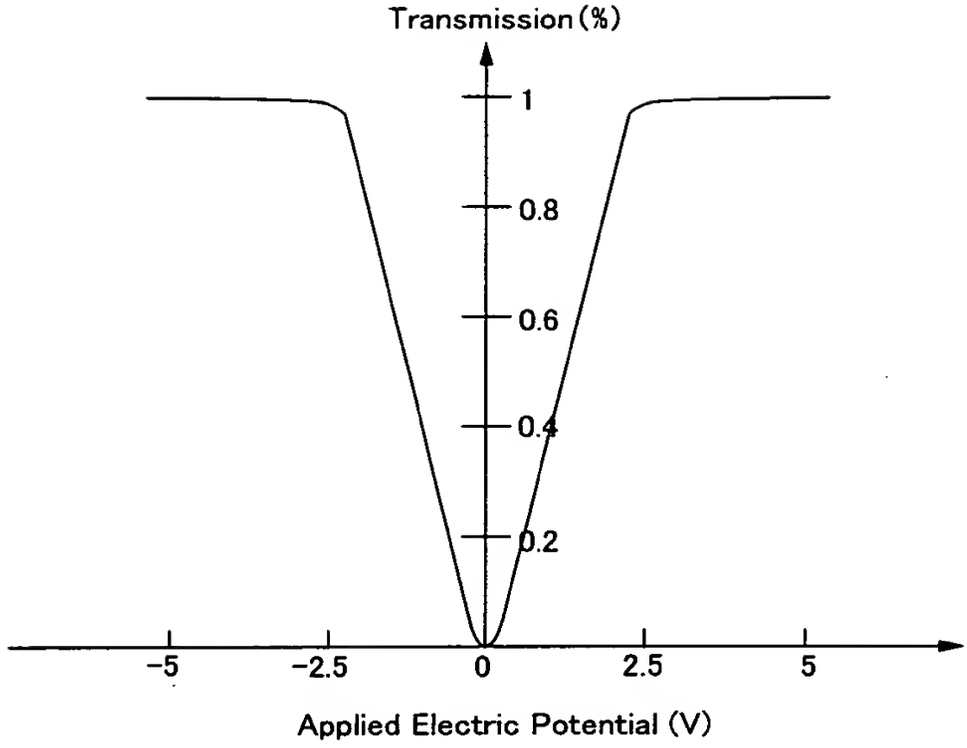

Transmission (%)
1
0.8
0.6
0.4
0.2
-5
-2.5
0
2.5
5
Applied Electric Potential (V)

Fig. 23(B)

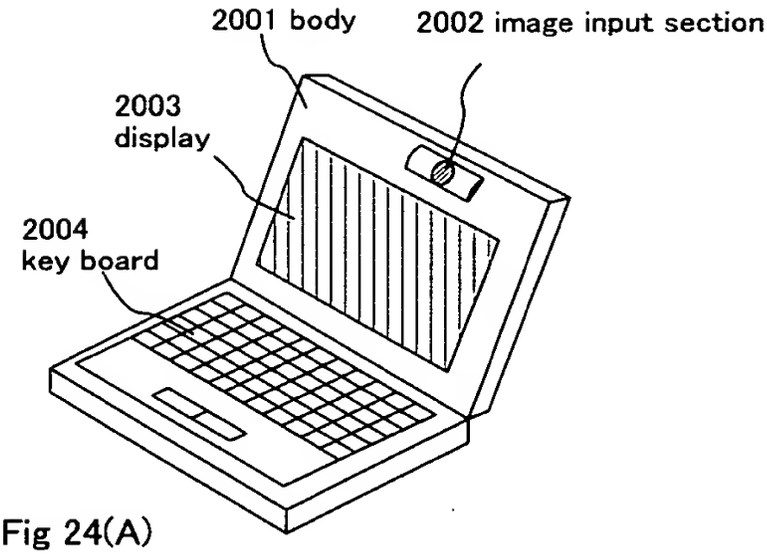


Fig 24(A)

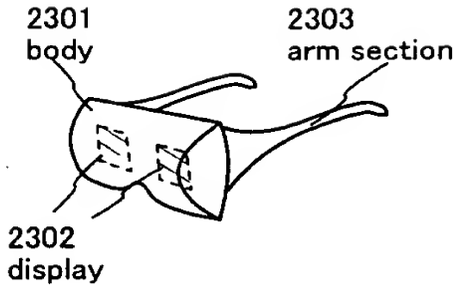


Fig. 24(B)

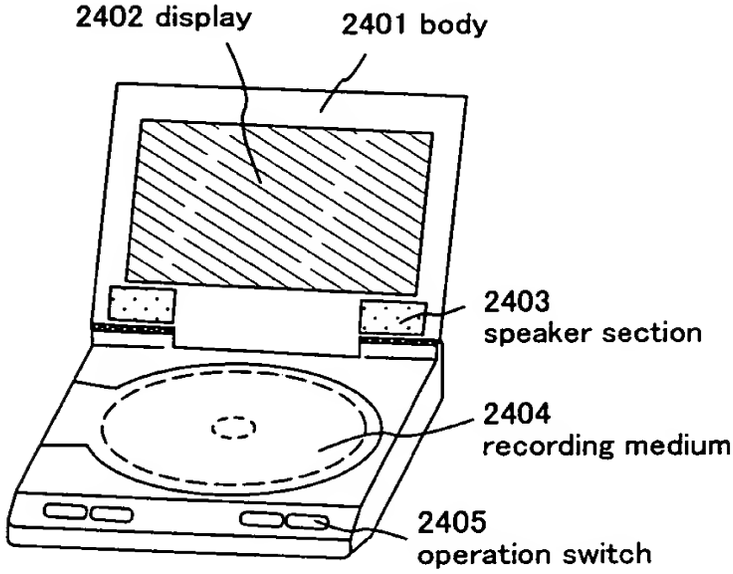


Fig. 24(C)

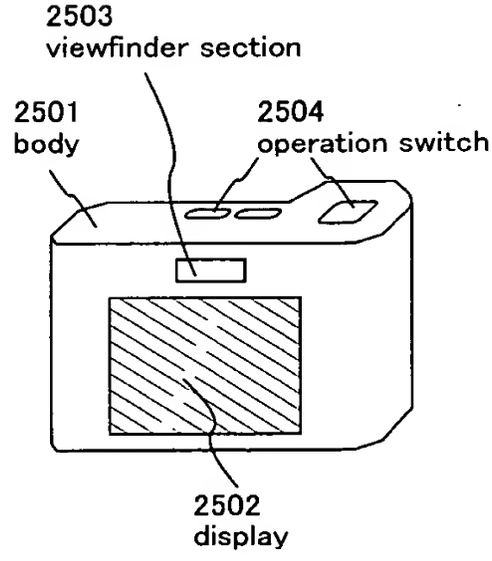


Fig. 24(D)